

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

1. (currently amended) A liquid crystal display comprising:

a plurality of gate lines disposed parallel to each other;

a plurality of data lines disposed parallel to each other and perpendicularly to said plurality of gate lines;

plural switching elements each disposed near an intersection between said plurality of gate lines and said plurality of data lines, each switching element having a source and a drain electrode, each said source and drain electrode overlapping one of said plural gate lines in plan view;

a plurality of pixel electrodes disposed over said gate lines and said data lines via an interlayer insulating film, so that in plan view, there is a gap between adjacent pixel electrodes, such that said gap between adjacent pixel electrodes at least partially overlaps with said gate line, an area where said source and drain electrodes overlap with said gate line to form the switching element being entirely outside said gap between adjacent pixel electrodes; and

a plurality of control electrodes each disposed directly under said gap between adjacent pixel electrodes and over said gate line, as seen in plan view, said control electrode directly overlies said gate line.

2. (previously presented) A liquid crystal display as set forth in claim 1, wherein, in plan view, said control electrode at least overlaps with said gap between adjacent pixel electrodes in width direction.

3. (previously presented) A liquid crystal display as set forth in claim 1, wherein, in plan view, said control electrode at least overlaps with an area in which said gate line and said gap between adjacent pixel electrodes overlap.

4. (previously presented) A liquid crystal display as set forth in claim 1, wherein, in plan view, said control electrode overlaps with said gate line and with said gap between adjacent pixel electrodes.

5. (previously presented) A liquid crystal display as set forth in claim 1, wherein said control electrode has the same potential voltage as that of said source electrode of said switching element.

6. (previously presented) A liquid crystal display as set forth in claim 1, wherein said control electrode is formed in the same layer as that of said source electrode of said switching element.

7. (previously presented) A liquid crystal display as set forth in claim 1, wherein said control electrode is formed integrally with said source electrode of said switching element.

8. (original) A liquid crystal display as set forth in claim 1, wherein said control electrode has a single layer structure comprising metal or ITO.

9. (original) A liquid crystal display as set forth in claim 1, wherein said control electrode has a multi-layer structure, each layer thereof comprising metal or ITO.

10. (original) A liquid crystal display as set forth in claim 1, wherein said interlayer insulating film comprises an organic film.

11. (original) A liquid crystal display as set forth in claim 1, wherein said liquid crystal display has a COT structure.

12. (original) A liquid crystal display as set forth in claim 1, wherein said liquid crystal display is a reflection type liquid crystal display.

13. (previously presented) A liquid crystal display as set forth in claim 1, wherein said control electrode and said source electrode of said switching element are coupled via an extended portion of said source electrode, and a contact hole for coupling said control electrode and said pixel electrode is provided on said extended portion.

14. (original) A liquid crystal display as set forth in claim 1, wherein a contact hole for coupling said control electrode and said pixel electrode is provided on said control electrode.

15. (original) An electronic equipment which includes said liquid crystal display as set forth in claim 1, in a display portion of said electronic equipment.

16. (currently amended) A liquid crystal display comprising:

an active matrix substrate;

an opposing substrate which is opposed to said active matrix substrate; and

a liquid crystal layer interposed between said active matrix substrate and said opposing substrate;

wherein said active matrix substrate comprises:

a plurality of gate lines which are disposed on an insulating substrate and which are disposed parallel to each other;

a plurality of data lines which are disposed on said plurality of gate lines via a gate insulating film and which are disposed parallel to each other and perpendicularly to said plurality of gate lines;

switching elements each disposed in the proximity of one of intersections between said plurality of gate lines and said plurality of data lines, each switching element having a

source and a drain electrode, each said source and drain electrode overlapping one of said plural gate lines in plan view;

a plurality of pixel electrodes disposed over said gate lines and said data lines via an interlayer insulating film, so that in plan view there is a gap between adjacent pixel electrodes, said gap between adjacent pixel electrodes at least partially overlaps with said gate line, an area where said source and drain electrodes overlap with said gate line to form the switching element being entirely outside said gap; and

a plurality of control electrodes each disposed directly under said gap between adjacent pixel electrodes and over said gate line, in plan view, said control electrode directly overlies said gate line.

17. (previously presented) A liquid crystal display as set forth in claim 16, wherein said control electrode is formed integrally with said source electrode of said switching element.

18. (previously presented) A liquid crystal display as set forth in claim 16, wherein said control electrode and said source electrode of said switching element are coupled via an extended portion of said source electrode, and a contact hole for coupling said control electrode and said pixel electrode is provided on said extended portion.

19. (original) A liquid crystal display as set forth in claim 16, wherein a contact hole for coupling said control

electrode and said pixel electrode is provided on said control electrode.

20. (previously presented) The liquid crystal display as set forth in claim 7, wherein for each said switching element, the control electrode covers a different said gate line than the gate line that drives the gate of the switching element.

21. (previously presented) The liquid crystal display as claimed in claim 4, wherein said control electrode completely overlaps said gate line in a width direction.

22. (currently amended) A liquid crystal display comprising:

a plurality of gate lines disposed parallel to each other;

a plurality of data lines disposed parallel to each other and perpendicularly to said plural gate lines;

plural switching elements each disposed near an intersection between said plural gate lines and said plural data lines, each switching element having a source and a drain electrode;

a plurality of pixel electrodes disposed over said gate lines and said data lines via an interlayer insulating film, in plan view, there is a gap between adjacent pixel electrodes, each said gap between adjacent pixel electrodes at least partially overlaps with one of said gate lines; and

a plurality of control electrodes, each of said plural control electrodes being directly under a first gap between a first set of adjacent pixel electrodes and directly overlying a first gate line, as seen in plan view, and extending to one of the switching elements whose gate is driven by a second gate line outside a second gap between a second set of adjacent pixel electrodes.